

# FACULTY PROFILE

**Snehal Julias Lopes** (snehalopes@sfitengg.org)

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Assistant Professor  
Department of Electronics & Telecommunication Engineering

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❖ Date of Joining the Institute: **01/07/2013**

❖ Education:

1. M.E. (Electronics and Telecommunication)  
SPIT, Andheri, 2014  
Thesis Title: Design and Analysis of communication circuits.
2. B.E. (Electronics)  
SFIT, Borivli, 2008  
Project: X-10 communication

❖ Total Experience in Years: **6.5 Years**

**Teaching: 6.5 Years**

- Assistant Professor: 6.5 years

❖ Courses Taught:

- Microelectronics(UG Level)
- Electronic instrumentation(UG Level)
- Electrical networks/Circuit theory and transmission lines(UG Level)
- Signals and systems(UG Level)
- Electronic devices and circuits(UG Level)
- Electronic Circuits and Communication Fundamentals(UG Level)
- Discrete time signal processing(UG Level)
- Optical fiber communication/ Optical communication network (UG Level)
- Analog and Digital Integrated Circuits (UG Level)
- Integrated circuits (UG Level).
- VLSI Design (UG Level)

❖ Skill Sets:

- Application software: PSpice,Hspice,MATLAB,VHDL,LTspice,Microwind
- Computer language: C,C++

❖ Publication & Conferences: **04**

**a. Conference**

National :-  
International : 2

**b. Journal**

National :-  
International : 2

❖ **Projects Guided :**

- B.E. Projects: **04**

❖ **Academic Duties:**

- Alumni committee, Exam Committee

❖ **Interaction with Professional Institutions :**

- University of Mumbai, Moderator and Examiner

❖ **R&D Activities:**

**A. Workshop, lecture series & Conferences Conducted:** workshop on LATEX, Google App workshop, Business Simulation Workshop(31<sup>st</sup> Dec.- 4<sup>th</sup> Jan 2017)

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