

# FACULTY PROFILE

**Mr. Ravindra Eknath Chaudhari** (ravindrachaudhari@sfitengg.org)

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Associate Professor  
Department of Electronics & Telecommunication Engineering

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❖ Date of Joining the Institute: **03/07/2003**

❖ Education:

1. Ph.D. Engineering (Pursuing)  
VNIT, Nagpur  
Dissertation Title: Fractal Video Compression Scheme
2. M.Tech. (Electronics Engineering)  
VRCE (VNIT), Nagpur, 2002  
Thesis Title: Design and Implementation of Functional Unit for Asynchronous DSP
3. B.E. (Electronics and Telecommunications Engineering)  
Govt. College of Engineering, Aurangabad, 1999  
Project:
4. D.I.E. (Industrial Electronics)  
SSGB Polytechnic, Bhusaval, 1996

❖ Total Experience in Years: **14.5 Years**

**Teaching: 13.5 Years**

- Associate Professor\*: 4.5 years
- Assistant Professor\*: 9 years
- \* Based on the equivalence of posts for the 4<sup>th</sup> and 5<sup>th</sup> Pay scales w.r.t. the 6<sup>th</sup> Pay scale
  
- **Courses Taught:**
- Image and Video Processing (UG Level)
- Electronic Devices & Circuits (UG Level)
- Discrete Time Signal Processing (UG Level)
- Elements Of Microelectronics (UG Level)
- Software Simulation Workshop (UG Level)
- Analog and Digital Integrated Circuits (UG Level).
- Lab.- Modern Digital Signal Processing (PG Level)
  
- **Skill Sets:**
- Application software: ORCAD PSpice, Microwind, L-Edit
- Computer language: C,

- Hardware language: VHDL, Verilog

❖ **Publication & Conferences: 10**

**a. Conference**

National : 3  
International : 3

**b. Journal**

National : 0  
International : 4

❖ Grants Fetched: NIL

❖ Awards: NIL

▪ **Professional Memberships:**

- IETE, ISTE

▪ **Projects Guided :**

- B.E. Projects: 15
- M.E. Project: 4

▪ **Academic Duties:**

- Coordinator of Department of Electronics & Telecommunication Engineering

▪ **Interaction with Professional Institutions :**

- University of Mumbai, Paper Setter, Moderator and Examiner
- NMIMS University, Paper Setter, Moderator and Examiner
- IIT, Deputy Presiding Officer for GATE-2010 exams held at SFIT
- DTE Mumbai: ARC coordinator, First year admission process CAP.

▪ **R&D Activities:**

▪ **Workshop, lecture series & Conferences Conducted:**

- Conducted session on “Use of Network Functions in Filter Theory” and was the Coordinator for 1 week STTP on ‘Advances in Electrical Networks’, July 7-11, 2008
- ELECTRO INFO COM-2007, National conference, Jan 4-6, 2007.
- Trends in Optical Network ISTE Approved at SFIT (21/05/2007 to 01/06/2007)
- Advances in Electrical Network (AEN-2008) ISTE Approved at SFIT, 7 and 11 July 2008.
- MIND’S EYE inter college project showcase Jun. 2006, April 2007 April 2009.
- One day workshop on ‘The Art of Setting Question Papers and Evaluation’, August 26, 2008
- One day Orientation Program on “Analog and Digital IC: Design & Application” (Sem.-IV, EXTC), Jan 31, 2009
- One day Orientation Program on “Principles of Control Systems” (Sem.-V, EXTC), July 31, 2009.
- A National Seminar on ‘Recent Developments in Circuits Systems and Signal Processing’, Feb 23-24, 2011

▪ **Continuing Education Programs**

- Faculty Development Program at SFIT, July 12, 2003 to July 26, 2003.
- Red Hat Enterprise server system Administration & network services with security administration, ISTE Approved at SFIT, 17 to 21 December, 2005.
- Emerging Microelectronics and VLSI Technologies for communication system ISTE approved at VCET, Vasai Road, 19 to 30 Jun, 2006.
- Trends in Optical Network ISTE Approved at SFIT, 21 May, 2007 to 01 Jun, 2007.
- Advances in Electrical Network (AEN-2008) ISTE Approved at SFIT, 07 to 11 July 2008.
- Developing managerial skills for senior technical academicians DISHA-09, at SFIT, 05 to 09 Jan 2009.
- CEP course on Elements of Microelectronics at IIT Bombay (4 days, only on weekends Septmber-2003)
- Faculty interaction program on Elements of Microelectronics subject, at VCET, July 15, 2004.
- One day workshop on advances in Microelectronics at VCET, Vasai Road, Aug. 23, 2003.
- MATLAB & Simulink at MIT, pune, 08 and 09 Oct. 2005.
- One day Orientation Program on “Software Simulation” (Sem.-IV, EXTC) at PVPPCE, Sion, 10 Feb. 2009.
- Participated in a Mission 10X workshop organized by WIPRO at FCRCE, Bandra, 23 to 27 Nov. 2009.

15/01/2017

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