

Minal Ajit Lopes (minallopes@sfitengg.org)

Assistant Professor
Department of information technology

❖ Date of Joining the Institute : **13/03/2003**

❖ Education :

1. Pursuing Ph.D. Electronics Engineering
Veermata Jijabai Technological Institute, Mumbai
2. M.E. Electronics
Fr. Concecao Rodrigues College of Engineering, Mumbai, 2008
Thesis Title: Performance Evaluation of Adaptive Equalization Techniques for Digital Mobile radio
3. B.E. Electronics
Veermata Jijabai Technological Institute, Mumbai, 2002
Project: Smart Card Reader

❖ Total Experience in Years : **14 Years 3 months(30/6/17)**

A. Teaching : 14 Years 3 Months

- Assistant professor: 14years 3 months

❖ Courses Taught:

- Next Generation Networks (PG Level)
- Digital Communication (UG Level)
- Principles of Communication (UG Level)
- Mobile Computing (UG Level)
- Networking Technology for digital devices (UG Level)
- Digital Signal & Image Processing (UG Level)
- Information and Network Security (UG Level)
- Image Processing (UG Level)
- System and Web security (UG Level)

❖ Skill Sets:

- Application software: MATLAB and Simulink, NS-2
- Computer language: C , Java

- ❖ Active Research in
 - Quantum Key Distribution
 - Wireless Networks

❖ Publication & Conferences : (30/6/17)

a. Conference

National : 02
International : 03

b. Journal

National : 00
International : 01

- Professional Memberships : **ISTE , IEEE**
- Projects Guided :
 - B.E Project: 23
- Interaction with Professional Institutions :
 - University of Mumbai as Examiner
- R&D Activities: developing “Faculty Information Management System” software
- **Continuing Education Programs**
 - **One week AICTE sponsored STTP on “Internet of Things”** organized by computer department VJTI, Mumbai from 15 March – 22 March, 2017.
 - Attended “**PHD Colloquium**” at VJTI Electronics Department, on 14-15 February 2017.
 - **Organized Colloquium on “IT for Society”**, at IT department, SFIT, Borivli during January 20-21,2017
 - Attended Colloquium on “**IT for Society**”, organized by IT department, SFIT, Borivli during January 29-30,2016
 - **One Week AICTE sponsored FDP on “RF Circuit Design”**, organized by VJTI, Mumbai during 9-13th Feb 2015.
 - **One week TEQIP Phase II sponsored STTP on “Research Methodology”**, organized by VJTI, Mumbai during February 3-7, 2014.
 - **One week TEQIP- STTP on “Information Security and Forensic Analysis”**, VJTI, 21-25 Oct 2013
 - **One week TEQIP STTP on “Advances in Signal and Image Processing”** GCOE, Jalgaon, 26 – 30 Nov 2012
 - **Two week AICTE-ISTE STTP on “Moving towards Open Source”** 25th June-6th July 2012
 - One day workshop on “**Ethical Hacking / Cyber Security (Ethical Hacker – level I)**” at Padmabhushan Vasantdada Patil College of Engg., Sion on 21st January 2012, organized by iSecLABz.

- **“MISSION10X”** Program Organized by Wipro Limited.
 - One Day Seminar on **“Power Electronics”** organized by FRCRCE, Bandra.
 - Short Term Training Program on **“Embedded DSP: Design and Application”** organized by SFIT, Borivli.
 - Short Term Training Program on **“Communication Systems”** organized by Thdomal Shahani Engineering College, Bandra.
 - **“Third in-House faculty development Program”** organized by SFIT, Borivli.
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