

# FACULTY PROFILE

**Dr. Gautam Abhaychand Shah** (gautamshah@sfitengg.org)

---

Professor & Head  
Department of Electronics & Telecommunication Engineering

---

❖ Date of Joining the Institute: **06/08/2001**

❖ Education:

1. Ph.D. Engineering  
MPSTME, NMIMS, Mumbai, 2012  
Dissertation Title: Algorithms and Architectures for Discrete Hartley Transform
2. M.E. (Digital Techniques & Instrumentation)  
SGSITS, Indore, 1995  
Thesis Title: Design and Development of Process Control Trainer
3. B.E. (Electronics)  
Walchand Institute of Technology, Solapur, 1990  
Project: Text in Picture

❖ Total Experience in Years: **26 Years**

**Teaching: 26 Years**

- Professor: 3 years
- Associate Professor: 7 years
- Assistant Professor: 16 years

❖ Courses Taught:

- Basic Electrical and Electronics Engineering (UG)
- Electronic Devices & Circuits (UG)
- Digital Logic Design (UG)
- Filter Theory (UG)
- Wave Shaping Techniques (UG)
- Analog and Digital Integrated Circuits (UG).
- Elements of Microprocessors (UG)
- Modeling and Simulation of Communication Systems (PG)

❖ Skill Sets:

- Application software: ORCAD PSpice
- Computer language: C, Pascal

❖ **Publication & Conferences: 21**

**a. Conference**

National : 5  
International : 8

**b. Journal**

National : 3  
International : 5

❖ **Grants Fetched: 1**

- Minor Research Grant (University of Mumbai, 2014-15)

❖ **Awards:**

- IETE K S Krishnan Memorial Award (2009)

❖ **Professional Memberships:**

- Senior Member-IEEE, Fellow-IETE, Fellow-IE, Member-ISTE

❖ **Projects Guided :**

- B.E. Projects: 10
- M.E. Projects: 2

❖ **Academic Duties:**

- Head, Department of Electronics & Telecommunication Engineering
- Member, Local Managing Committee
- Chief / Joint Chief conductor for University examinations
- Member, Academic Advisory Committee

❖ **Interaction with Professional Institutions :**

- University of Mumbai, Paper Setter, Moderator and Examiner
- IIT, Presiding / Deputy Presiding Officer for IIT-JEE exams held at SFIT
- University of Mumbai, Member, Syllabus Revision committee in Electronics & Telecommunication Engineering.

❖ **R&D Activities:**

**A. Workshop, lecture series & Conferences Conducted:**

- Conducted session on “Use of Network Functions in Filter Theory” and was the Coordinator for 1 week STTP on ‘Advances in Electrical Networks’, July 7-11, 2008
- Conducted session on “Evaluation of Answer-Scripts” in the workshop on ‘The Art of Setting Question Papers and Evaluation’, August 26, 2008
- Conducted sessions on “Lesson Plan / Session Plan, Planning for the term and preparation of the Course File” in the 2 week ISTE approved STTPs on ‘Faculty Development (Induction) Program’, June 2006, June 2007 and July 2010
- Co-Convener for 2 day National Seminar on ‘Recent Developments in Circuits Systems and Signal Processing’, Feb 23-24, 2011
- Conducted session on “Hartley Transform” in the 1 week ISTE approved STTP on Mathematical & Computational Techniques in Engineering, March 26-30, 2012
- Member of Core committee and Paper Review committee in the International Conference on Circuits, Systems, Communication and Information Technology Applications, April 4-5, 2014
- Conducted session on “Course file compilation and Rubric design techniques for outcome evaluation in the 1 week STTP on ‘Faculty Induction Program’, July 6-10, 2015

## **B. Invited Talks**

- “Current Trends in the Mixed-Mode Signal Processing Architectures”, National Conference on Information and Communication Technology, New Horizons in Technology and Applications, NCICT-2007, Mar 1-3, 2007, DJSCOE & NMIMS University, Vile Parle, Mumbai
- “Architectures for Discrete Hartley Transform”, National Conference on Communications and Signal processing, NCCSP-2007, Mar 12-13, 2007, Thadomal Shahani Engineering College, Bandra
- “Miller Equivalents and Their Applications”, National Conference on Electronics, Communications and Computers, NCECC-2009, February 13-14, 2009, Chandragupt Hall, Abbott Hotel, Vashi, Navi Mumbai, IETE Navi Mumbai Sub Centre

## **C. Continuing Education Programs**

- 2 week STTP, Faculty Development Programme, Sep 15, 2001 – Feb 9, 2002, SFIT, Mumbai
- 2 day Seminar, The Bridges for Electronics, Sep 12-13, 2003 at DBIT, Mumbai
- 1 week STTP, Accreditation of Technical Institutions, Dec 13-17, 2004, NITTTR, Chandigarh
- 1 day Workshop, National Programme for Technology Enhanced Learning, July 28, 2006, C-DEEP, IIT Bombay
- 2 day International Conference, IETE International Conference on Nanotechnology and Health Care Applications, October 12-13, 2007, CETTM, Hiranandani Gardens, Powai
- 1 week ISTE approved STTP, Mathematical & Computational Techniques in Engineering, March 26-30, 2012, SFIT, Mumbai
- 2 week ISTE Workshop, Analog Electronics, June 4 - 14, 2013, SFIT, Mumbai conducted by IIT Kharagpur under the National Mission on Education through ICT (MHRD)
- 2 week ISTE Workshop, Signals and Systems, January 2 - 12, 2014, SFIT, Mumbai conducted by IIT Kharagpur under the NMEICT (MHRD).
- Elsevier connect seminar, November 2016, Marriot, Mumbai.