

# FACULTY PROFILE

**Dr. Deepak. J. Jayaswal** (djjayaswal@sfitengg.org)

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Professor & Dean M.E programme  
Department of Electronics & Telecommunication Engineering

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❖ Date of Joining the Institute: **09/08/2005**

❖ Education:

Ph.D. Computer Engineering National Institute of Technology, Surat, 2010  
Dissertation Title: Optimal Search Algorithm for Motion Estimation.

M.Tech Communication Indian Institute of Technology Bombay (IITB), 2001  
Thesis Title: Scalable Video Coding.

B.E. Electronics Walchand College of Engineering, Sangli, 1991  
Project: Video Effect Generator.

❖ Total Experience in Years: **26.5 Years**

**Industry Experience: 03 years**

- Member R&D, Crystal Audio Private Limited, 03 years

**Teaching: 23 Years**

- Professor: 5.5 year
- Associate Professor: 10 years
- Assistant Professor: 08 years

❖ Courses Taught:

- Digital signal processing(UG)
- Image Processing(UG)
- Signal and System(UG)
- Communication systems(UG)
- Computer Networks(UG)
- Analog Integrated Circuits(UG)
- Wave Shaping techniques(UG)
- Electronic Devices and Circuits(UG)
- Basic Electrical and Electronics engineering(UG)
- Advanced Digital Signal processing(PG)
- Adaptive Signal Processing(PG)
- Research Methodology(PG)

## Syllabus designed :

- Discrete Time signal Processing
- Adaptive signal processing
- Devices and circuits
  
- ❖ **Skill Sets:**  
Application software: Matlab and Simulink, NS-2, NETSIM, SCILAB, Open CV, PRATT, ORCAD, Micro Wind, Multisim, PSpice.
  
- ❖ **Publication & Conferences: 29**
  - a. **Conference :**  
International : 14
  - b. **Journal**  
National : 3  
International : 12
  
- ❖ **Professional Memberships:**
  - IEEE(USA) member, IETE(I) member, ISTE (I) member ,CSI (I) member
  
- ❖ **Projects Guided :**
  - B.E. Projects:42
  - M.E. Project:10
  - PhD : 06 (student under guidance)
  
- ❖ **Academic Duties:**
  - HOD : Information Technology Department (Vidyavardhini's College of Engineering and Technology)
  - NBA coordinator: St. Francis Institute of technology(SFIT), Mumbai-400103
  - Chief conductor for IIT-JEE exam at SFIT
  - Chief conductor for GATE exam at SFIT
  - Dean M.E at SFIT St. Francis Institute of technology(SFIT), Mumbai-400103
  - Member Curriculum Development Council- Mumbai University
  - Member Academic Advisory Committee St. Francis Institute of technology(SFIT), Mumbai-400103
  
- ❖ **Interaction with Professional Institutions :**
  - LIC visit for Mumbai University
  - Organizing University Principals' meet
  - Setting the question papers, evaluation of answer sheet, conducting the practical examinations at UG, PG levels
  - Attended meetings and workshop conducted by other universities
  - Conducting and Attending Orientation programs
  
- ❖ **R&D Activities:**
  - A. **Workshop, lecture series & Conferences Conducted:**
    - R&D lecture series
    - One Day workshop on 'Matlab and Simulink' August 2003

- Server System Administration and Network Services with Security Administration' December 17-23, 2005
- One day seminar on 'Neural Network & Fuzzy System' February 21, 2005
- National Conference on INFOCOM, 2007
- One week ISTE-STTP on 'Advances in Electrical Network' 2008
- Two days national seminar on 'Recent Development in Circuits System and Signal Processing' February 23-24, 2011
- International Conferences, April 2014

### **B. Invited Talks**

- Digital signal processing and its application
- Role of NBA in educational quality assurance
- Accreditation Awareness

### **C. Continuing Education Programs**

- Summer School on Music Science Technology and Research (MuSTeR) IISC Bangalore May, 2016.
- One week FDP on "Advances in Wireless Communication" organized by VJTI, Mumbai 2014
- One - Week Co-ordinator's Workshop on Signals & Systems to be held during 30th September 2013 - 4th October 2013 by National Mission on Education Through ICT
- Two week ISTE workshop on Analog Electronics by National Mission on Education Through ICT, MHRD-India, Nov 2013
- Four days' workshop on "Outcome Based Accreditation for Stakeholders" held on 29<sup>th</sup> April 2013 and 9<sup>th</sup> -11<sup>th</sup> May 2013.
- Two week ISTE STTP on Networking: Theory to Practice.
- Two days' workshop on Matlab and Simulink.
- Two days' workshop on Object Oriented Analysis and Design.
- Two week ISTE STTP on Mechatronics.
- Two week ISTE STTP on Industrial Automation.
- One week ISTE STTP on Advanced microcontroller and embedded systems.
- One day seminar on Neural Network and Fuzzy System
- 5-credit course in Embedded System Design Using Microcontroller
- Two week ISTE STTP on DSP & its Application
- One week ISTE STTP on Linux.
- Organized and participated in two weeks ISTE STTP on Trends in optical fiber network.
- Organized ISTE approved STTP on Advances in Electrical Network
- One week STP of 5-credit course on Embedded system Design.
- One week ISTE STTP on "Developing Managerial Skills for Senior Technical Academicians"
- One week AICTE sponsored STTP on Computer Vision and Image Processing-2008
- Conducted one week STTP on Linux
- Conducted SFIT National Conference on Trends and Challenges in Electronics, communications, Computer & Information Technology , Jan 4-6, 2007