

ST. FRANCIS INSTITUTE OF TECHNOLOGY
Mount Poincur, SVP Road, Borivali (W), MUMBAI - 400103.

SFIT/EXTC/F-04/REV0

Electronics & Telecommunication Department
Class Time Table (CTT)

Timing:- 8.00 am to 5.00 pm
Room no. : 510
Class:- SE EXTC-A
Branch: Electronics & Telecommunication

Batches : SA1 Roll no: 1-25
SA2 Roll no: 26-50
SA3 Roll no: 51-75

Academic Year 2017-18 (July17-Dec 17)

wef: 10th July, 2017

Regular Periods	Monday	Tuesday	Wednesday	Thursday	Friday
8 TO 9			EIC L QS	CT&N L SwC	AM-III L RP
9 TO 10	EIC L QS	EDC-I L JS	AM-III L RP	AM-III L RP	EDC-I L JS
10.00 TO 10.15	TEA BREAK				
10.15 TO 11.15	DSD L LP	DSD P SA1 501B	OOPM P SA2 112 PK	EDC-I P SA3 509B SwC	CT&N L SwC
11.15 TO 12.15	EIC T QS			EIC T QS	DSD L LP
12.15 TO 1.00	LUNCH BREAK				
1.00 TO 2:00	AM-III L RP	CT&N L SwC	DSD L LP	EIC L QS	EIC L QS
2:00 TO 3:00	EDC-I L JS	AM-III T AD	EDC-I L JS	CT&N T SwC	CT&N T SwC
3:00 TO 4:00	OOPM P SA1 112 PK	EDC-I P SA2 509B SwC	DSD P SA3 501B QS	AM-III T RP	DSD L LP
4:00 TO 5:00					

Abbreviation : L: LECTURE , P: PRACTICAL , T: TUTORIAL

AM-III	Applied Mathematics-III	AD	Ms.Astrid D'souza
		RP	Ms. Rachel Pereira
EIC	Electronics Instrumentation and Control	QS	Ms. Quantah Sheikh
		LP	Ms. Lakshmi Prabhu
DSD	Digital System Design	QS	Ms. Quantah Sheikh
		JS	Ms Jovita Serrao
EDC-I	Electronic Devices and Circuits-I	SwC	Ms. Swapnil Chilap
		SwC	Ms. Swapnil Chilap
CT&N	Circuits Theory and Networks	SwC	Ms. Swapnil Chilap
OOPM	Object Oriented Programming Methodology	PK	Ms.Priya Karunakaran

Prepared by: Ms. Namrata Mankad & Ms. Savita kulkarni
Ms Vrishali, Ms Seema

Ms. Mrinmoyee Mukherjee
Convenor

Dr. Gautam Shah
HOD

Dr. Sincy George
Principal