

ST. FRANCIS INSTITUTE OF TECHNOLOGY
Mount Poinzur, SVP Road, Borivali (W), MUMBAI - 400103.

SFIT/EXTC/F-04/REV0

Electronics & Telecommunication Department
Class Time Table (CTT)

Timing:- 8.00 am to 5.00 pm
Room no. : 510
Class:- SE EXTC-A
Branch: Electronics & Telecommunication

Batches : SA1 Roll no: 1-24
SA2 Roll no: 25-48
SA3 Roll no: 49-72

Academic year 2017-18 (Jan 18'-June 18')

8th Jan, 2018

Regular Periods	Monday	Tuesday	Wednesday	Thursday	Friday			
8 TO 9					PCOM L VA			
9 TO 10	SS L QS	SS L QS	EDC-II L JS	PCOM L VA	LIC L GS			
10.00 TO 10.15	TEA BREAK							
10.15 TO 11.15	LIC L GS	AM-IV T GJ /AD Room No 510 & 502	EDC-II L JS	EDC-II L JS	EDC-II P SA1 509B JS	LIC P SA2 501B SwC	PCOM P SA3 611B VA	
11.15 TO 12.15	AM-IV L GJ	LIC L GS	AM-IV L GJ	LIC L GS				
12.15 TO 1.00	LUNCH BREAK							
1.00 TO 2:00	EDC-II L JS	PCOM P SA1 611B VA	EDC-II P SA2 509B JS	LIC P SA3 501B SwC	SS L QS	AM-IV L GJ	AM-IV T GJ /AD Room No 510 & 512	
2:00 TO 3:00	PCOM L VA				PCOM L VA	SS L QS	AM-IV L GJ	
3:00 TO 4:00		SS T QS (Roll no 1-36)		T&P session	LIC P SA1 501B SwC	PCOM P SA2 611B VA	EDC-II P SA3 509B JS	SS T QS (Roll no 37 onwards)
4:00 TO 5:00								

Abbreviation : L: LECTURE , P: PRACTICAL , T: TUTORIAL

Subjects : Legends

AM-IV	Applied Mathematics-IV	GJ	MS. Gauri Jagusthi
		AD	Ms. Astrid Dsouza
EDC-II	Electronic Devices and Circuits II	JS	Ms. Jovita Serrao
LIC	Linear Integrated Circuits	GS	Dr. Gautam Shah
SS	Signals & Systems	QS	Ms. Quantiah Shaikh
PCOM	Principles of Communication Engineering	VA	Mr. Vaqar Ansari
EDC-II LAB	Electronic Devices and Circuits II Laboratory	JS	Ms. Jovita Serrao
LIC LAB	Linear Integrated Circuits Laboratory	SwC	Mr. Swapnil Chilap
PCOM LAB	Principles of Communication Engineering Laboratory	VA	Mr. Vaqar Ansari
SS LAB	Signals & Systems Laboratory	QS	Ms. Quantiah Shaikh

Prepared by: Ms Namrata Mankad & Ms Savita Kulkarni

Ms. Savita Kulkarni
Convenor

Dr. Gautam Shah
HOD

Dr. Kevin Noronha
Dean Academics